

Amendments to the Claims

This listing replaces all prior versions, and listings, of claims in the application:

**Listing of Claims**

1. (Currently Amended) A switching amplifier, comprising:  
  
a power stage having an input and an output, the output providing an amplified pulse modulated output signal; and  
  
a digital correction circuit having a first input for receiving a pulse modulated input signal, a second input coupled to the output of the power stage, and an output coupled to the input of the power stage, the digital correction circuit providing a corrected pulse modulated signal to the input of the power stage, and having multiple correction modes dependent upon ~~corrected as a function of~~ a duty ratio of said pulse modulated input signal.
2. (Canceled)
3. (Original) The switching amplifier of claim ~~2~~<sup>1</sup>, the digital correction circuit performing, in each correction mode, a discrete-time pulse edge correction on at least one of a leading edge and a trailing edge of the pulse modulated input signal to provide the corrected pulse modulated signal.
4. (Original) The switching amplifier of claim ~~2~~<sup>1</sup>, the digital correction circuit further comprising, an analog to digital converter having an input coupled to the output of the power stage and an output providing a digital correction signal, a sampling time of the analog to digital converter changing as a function of said duty ratio.

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5. (Original) The switching amplifier of claim ~~2~~<sup>1</sup>, the digital correction circuit further providing for smooth transitions between said multiple correction modes.

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6. (Original) The switching amplifier of claim 1, the digital correction circuit further comprising:

an error amplifier having a first input for receiving a pulse modulated reference signal,  
and a second input for receiving the output of the power stage, and an output for providing an analog correction signal;  
an analog to digital converter coupled to the error amplifier for converting the analog correction signal into a digital correction signal; and  
a digital pulse edge corrector having a first input connected to the output of the analog-to-digital converter, a second input for receiving the pulse modulated input signal and an output coupled to the input of the power stage.

7. (Original) The switching amplifier of claim 6, the analog to digital converter having a controllable sampling time that varies as a function of said duty ratio.

8. (Original) The switching amplifier of claim 6, the error amplifier comprising a multiple order error amplifier.